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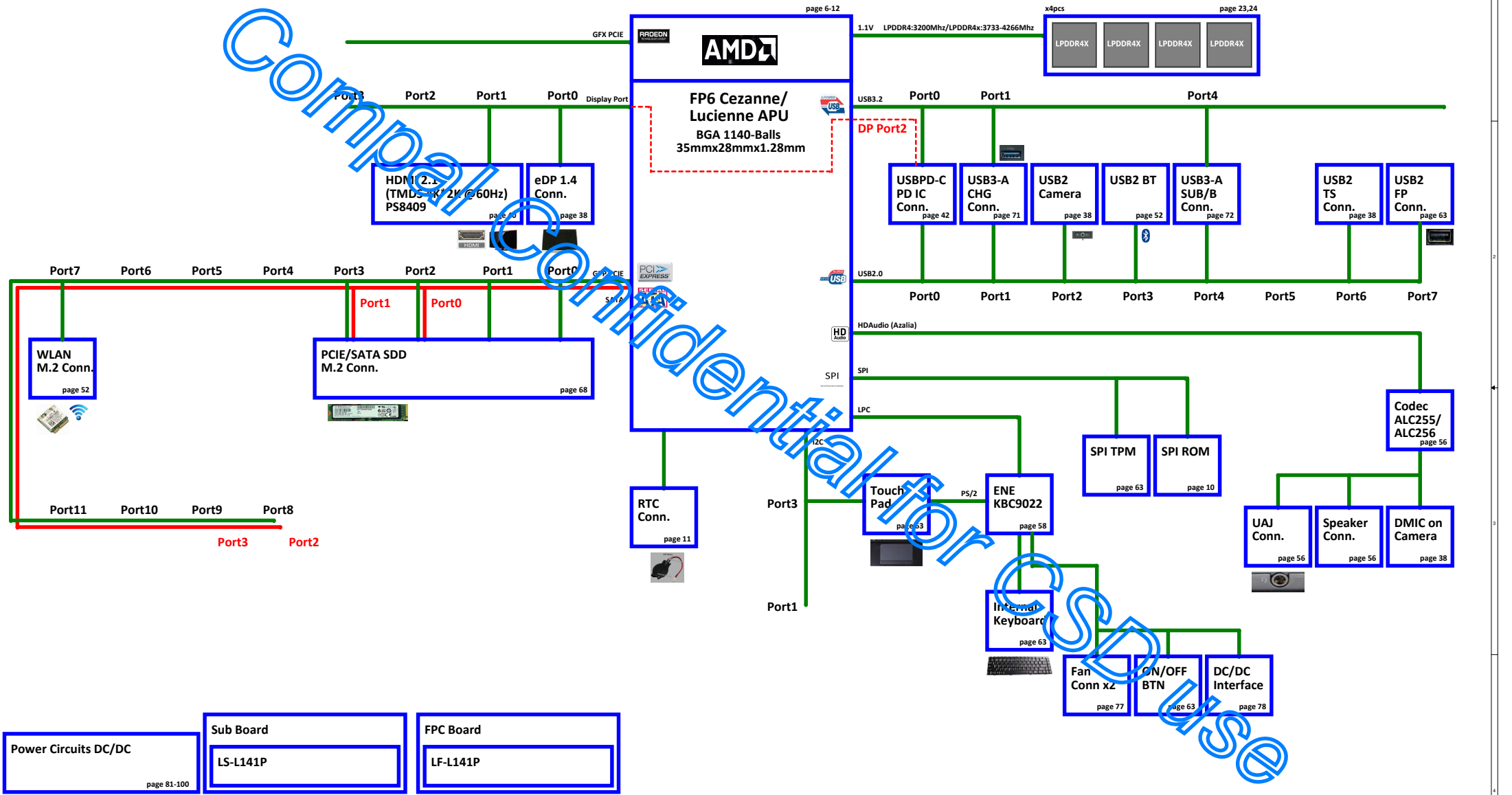
GH4UZ Schematics Document

AMD Cezanne/Laurenne (FP6) Platform

LA-L141P REV:1B

2021-01-12

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
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				COVER PAGE	
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Power Plane	Description	S0	S3	S5
+19V_VIN	Adapter power supply (19V)	ON	ON	ON
+19VB	AC or battery power rail for power circuit.	ON	ON	ON
+APU_CORE	Core voltage for APU	ON	OFF	OFF
+APU_CORE_SOC	Voltage for On-die VGA of APU	ON	OFF	OFF
+0.75VALW	0.75V always on power rail	ON	ON	OFF
+0.75VS	0.75V switched power rail	ON	OFF	OFF
+1.8VALW	1.8V always on power rail	ON	ON	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+1.8V	1.8V power rail for APU and DDR	ON	ON	OFF
+1.1V	1.1V power rail for APU and DDR	ON	ON	OFF
+MEM_VDDQ	1.1V/0.6V switched power rail for LPD_R/4x	ON	ON	OFF
+3VALW	3.3V always on power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	AC-ON
+5VS	5V switched power rail	ON	OFF	DC-ON DC-OFF
+RTC_APU	RTC power	ON	ON	ON
+LCDVDD	3.3V panel logic power	ON	OFF	OFF
+5VS_BL	5V Keyboard Backlight power	ON	OFF	OFF
+USB3_VCCA	USB Type-A Conn. power	ON	OFF	OFF
+VBUS_CONN	USB Type-C Conn. power	ON	OFF	OFF

Power Plane	Description	S0	S0i3	S5
+TP_VCC	3.3V Touch Pad power	ON	ON	OFF
+TS_PWR	3.3V Touch Screen Power	ON	OFF	OFF
+FP_VCC	3.3V Finger Print power	ON	ON	OFF
+3V_SSD	3.3V PCIE/SATA SSD	ON	OFF	OFF
+3V_WLAN	3.3V WLAN power	ON	ON	ON
+VDDA	5V Audio Codec power	ON	ON	ON
+3V_CAM	3.3V Camera power	ON	ON	OFF
+DMIC_VCC	1.8V DMIC power	ON	ON	ON
+VDDIO_AUDIO	1.8V APU Audio power	ON	ON	ON
BT_ON	Follow SYS0N	ON	ON	OFF
WL_OFF#	Follow SYS0N	ON	ON	OFF

Master	Device	Address[7:1]	Address [7:0]	
			Write	Read
I2C Port 0 (+1.8VS)				
I2C Port 1 (+1.8VS)	G-Sensor (Reserver)	0001 1000b 18h	0011 0000b 30h	0011 0001b 31h
I2C Port 2 (+3VS)				
SBMUS Port 0 (+3VS)	JDIMM1	0101 0000b 50h	1010 0000b A0h	1010 0001b A1h
	JDIMM2	0101 0001b 51h	1010 0010b A2h	1010 0011b A3h
I2C Port 3 (+3VALW)	PTP (Synaptics)	0010 1100b 2Ch	0011 1000b 58h	0011 1001b 59h
	PTP (ELAN)	0001 1111b 15h	0011 1110b 3Eh	0011 1111b 3Fh
SMBus Port 1 (+3VALW)				

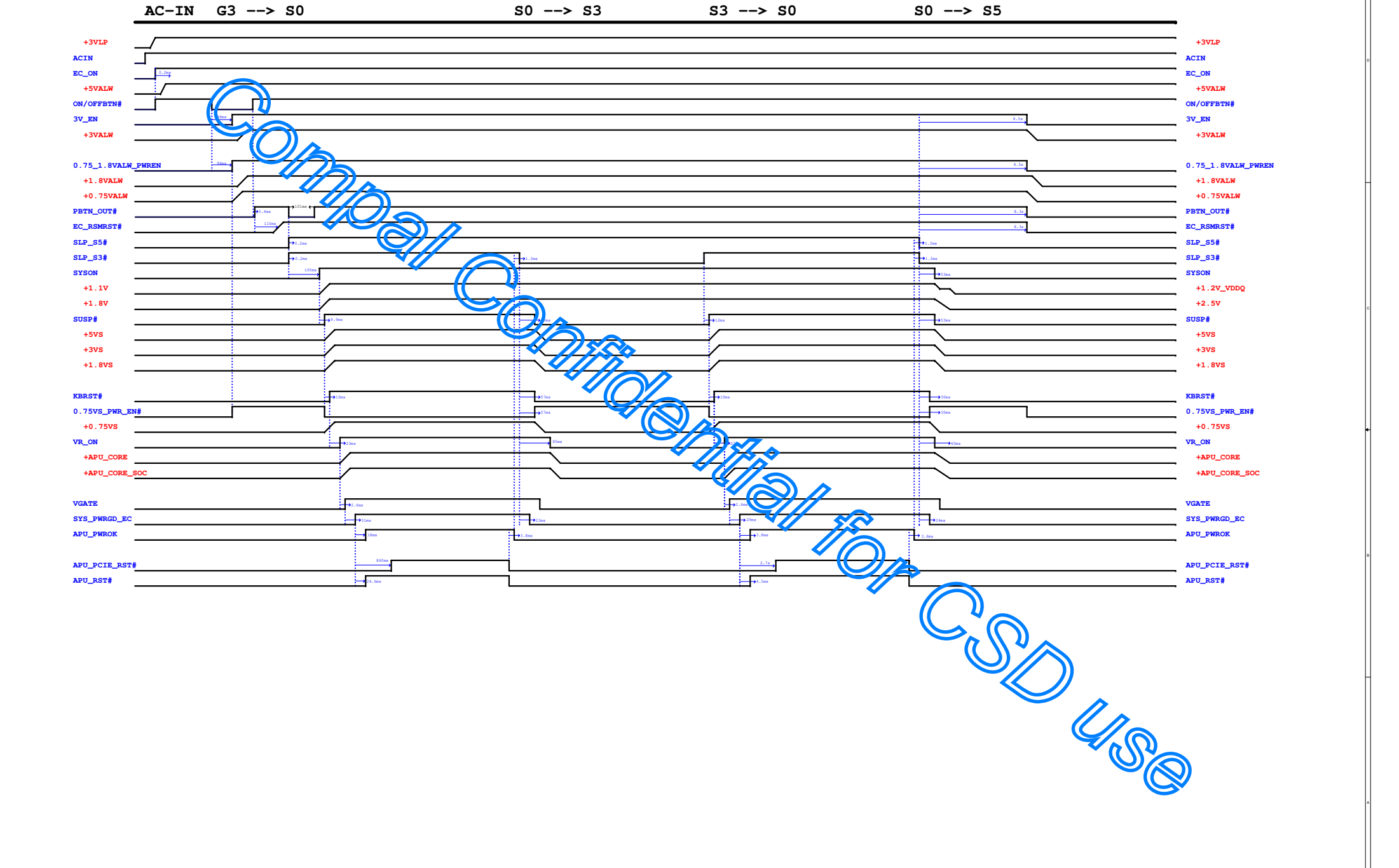
Board ID	PCB Revision
0	EVT
1	DVT
2	PVT

Config	Config Description
@	Components Do Not Install
45@	HDMI Royalty PN
CONN@	Mechanical Connector
JP@	Jump
RS@	R-Short
TP@	Test Point
WDT@	AMD HDT Debug Components
TPM @	TPM Components
USB Charger/G@	USB Charger/Non-Charger
AL255@	Audio Codec AL255 Design
AL256@	Audio Codec AL256 Design
FPC@/2FSD@	Finger Print Components
EMI@/@F@	EMI Components Install/Uninstall
ESD@/@ESD@	ESD Components Install/Uninstall
I2CTS@	For I2C touch screen
USBTs@	For USB touch screen

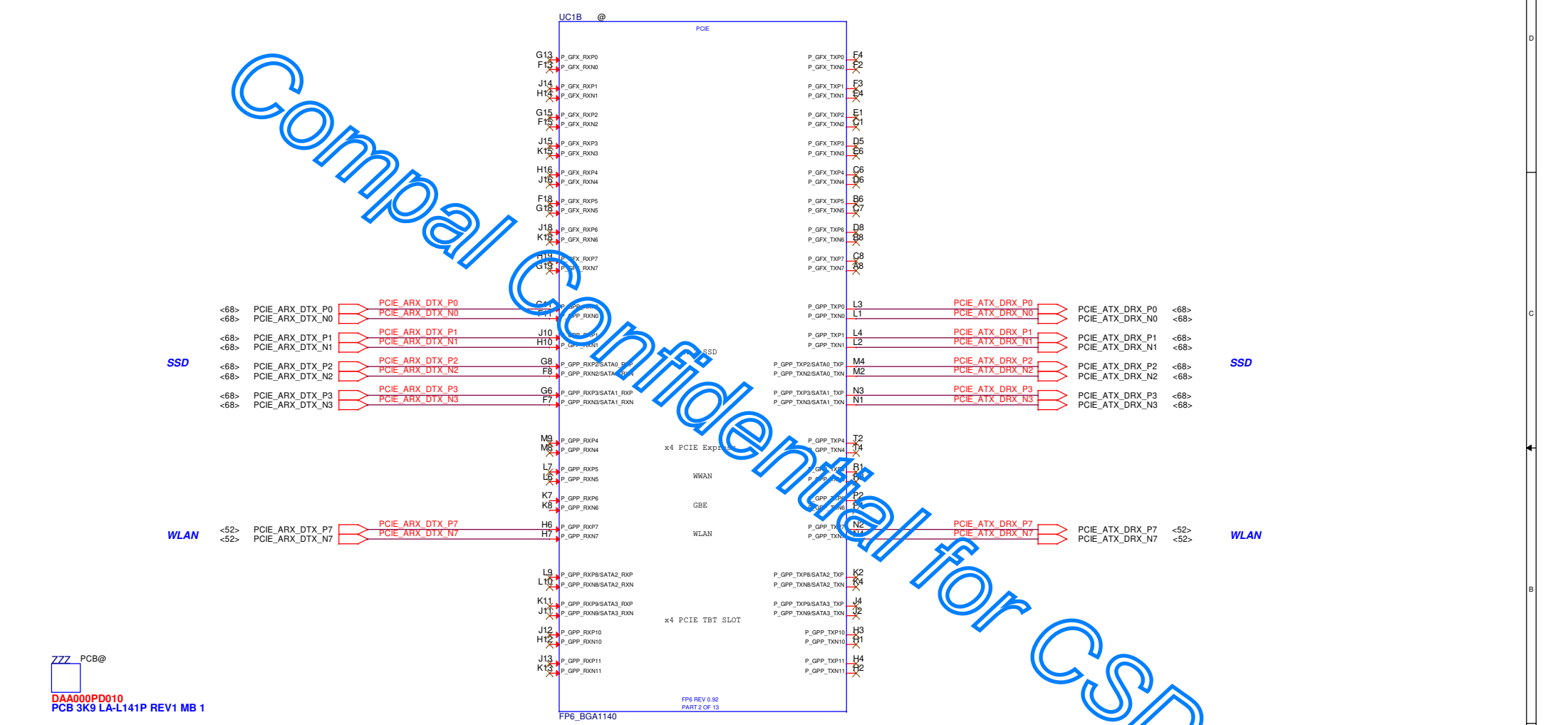
SMBus Port 1 (+3VALW)	Smart Battery	0000 1011b 0Bh	0001 0110b 16h	0001 0111b 17h
	Charger IC	0000 1001b 09h	0001 0010b 12h	0001 0011b 13h
SMBus Port 2 (+EC_VCC)	APU Temp.	0100 1100b 4Ch	1001 1000b 98h	1001 1001b 99h
	TPS65987	0010 0011b 23h	0100 0110b 46h	0100 0111b 47h
	Therm Sensor Temp.	0100 1000b 48h	1001 0000b 90h	1001 0001b 91h
	HDMI Retimer PS8409A	0001 0000b-0010 1111b 10h-2Fh		

Vcc	3.3V				
Ra	100K +/- 1%				
Board ID	Rb	V min	V typ	V max	EC AD
0	0		0.000V	0.300V	0x00 - 0x0B
1	12K +/- 1%	0.347V	0.354V	0.360V	0x0C - 0x1C
2	15K +/- 1%	0.423V	0.430V	0.438V	0x1D - 0x26
3	20K +/- 1%	0.541V	0.550V	0.559V	0x27 - 0x30
4	27K +/- 1%	0.691V	0.702V	0.713V	0x31 - 0x3B
5	33K +/- 1%	0.807V	0.819V	0.831V	0x3C - 0x46
6	43K +/- 1%	0.978V	0.992V	1.006V	0x47 - 0x54
7	56K +/- 1%	1.169V	1.185V	1.200V	0x55 - 0x64
8	75K +/- 1%	1.398V	1.414V	1.430V	0x65 - 0x76
9	100K +/- 1%	1.634V	1.650V	1.667V	0x77 - 0x87
10	130K +/- 1%	1.849V	1.865V	1.881V	0x88 - 0x96
11	160K +/- 1%	2.015V	2.031V	2.046V	0x97 - 0xA3
12	200K +/- 1%	2.185V	2.200V	2.215V	0xA4 - 0xAD
13	240K +/- 1%	2.316V	2.329V	2.343V	0xAE - 0xB7
14	270K +/- 1%	2.395V	2.408V	2.421V	0xB8 - 0xC0
15	330K +/- 1%	2.521V	2.533V	2.544V	0xC1 - 0xC9
16	430K +/- 1%	2.667V	2.677V	2.687V	0xCA - 0xD3
17	560K +/- 1%	2.791V	2.800V	2.808V	0xD4 - 0xDC
18	750K +/- 1%	2.905V	2.912V	2.919V	0xDD - 0xE6
19	NC	3.000V	3.300V		0xEF - 0xFF








Signal	Pin	Function
G-A	1	+RTCBATT
	2	EC_ON
	3	+5VALW
G-B	4	3V_EN
	5	+3VALW
	6	0.75_1.8VALW_PWREN
G-C	7	+1.8VALW/+0.75VALW
	8	SYSON
	9	+1.1V/+1.8V
G-D	10	SUSP#
	11	+5VS/+3VS/+1.8VS/+MEM_VDDO
	12	0.75VS_PWR_EN#
G-E	13	+0.25VS
	14	VALW_PN
	15	+APLW_CORE
G-F	16	+APLW_CORE_S0C
	17	



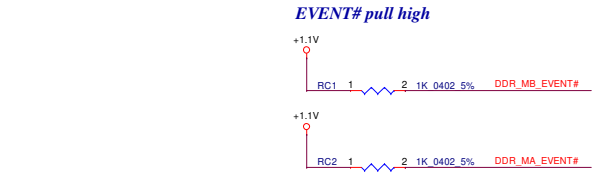
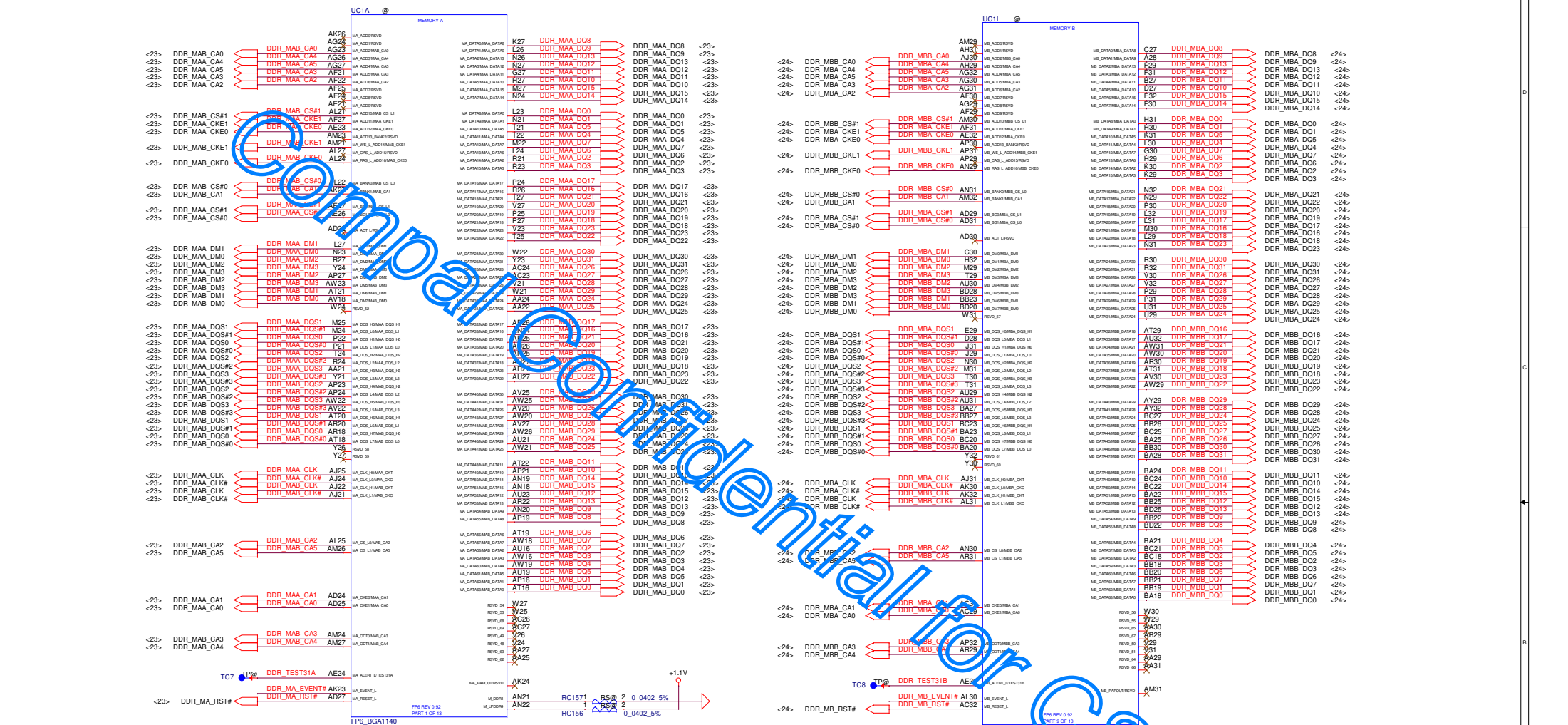
Main Func = CPU



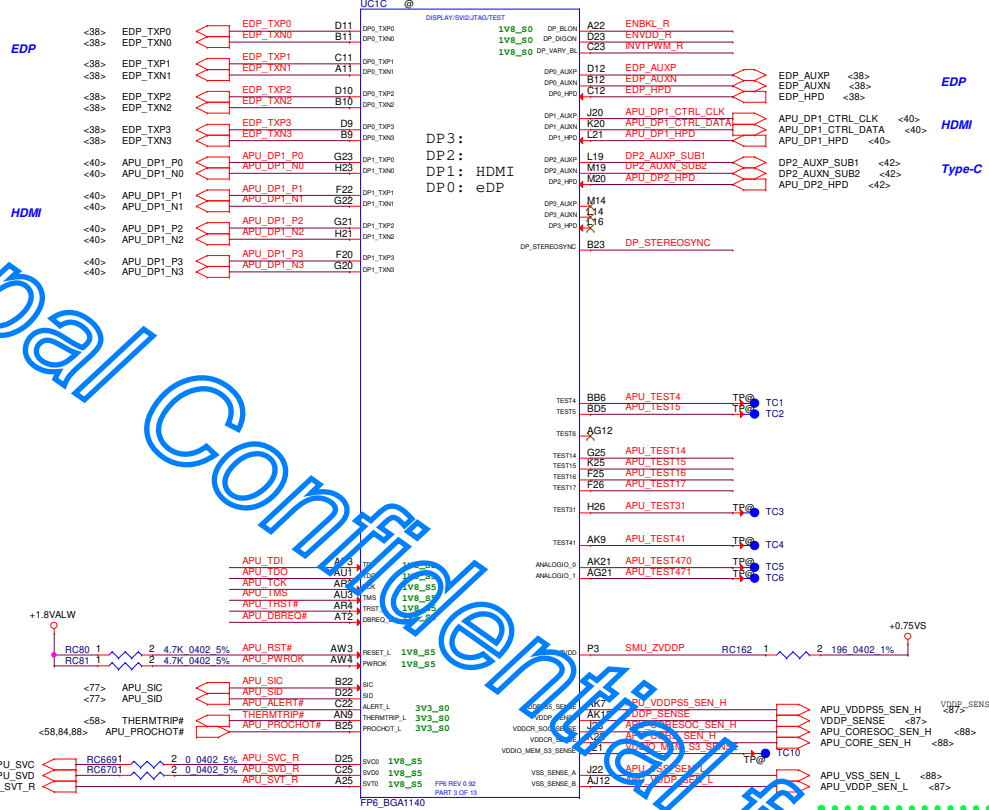
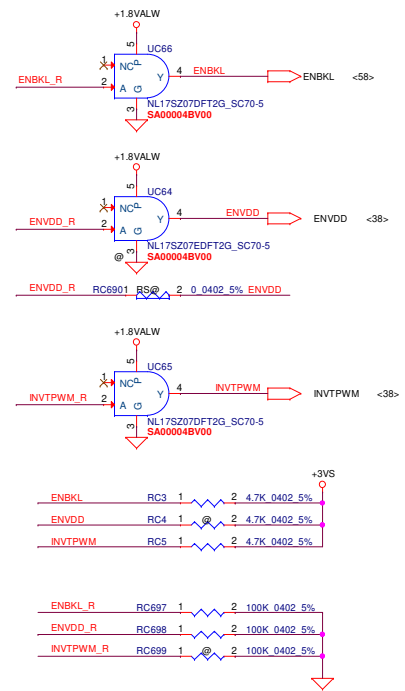
APU PN Table

APU Platform	Customer PN Cezanne	Customer PN Cezanne	Customer PN Cezanne	Customer PN Cezanne	Customer PN Lucienne	Customer PN Lucienne	Customer PN Lucienne
Renoir	UC1  100-000000287-40 R5CZS_U_PC@ SA0000E1000 S IC RYZEN5 100-000000287-40 2.1G APU S	UC1  100-000000285-40 R7CZS_U_PC@ SA0000DWS10 S IC RYZEN7 100-000000285-40 1.8G APU S	UC1  100-000000295-40 R5CZS_H_PC@ SA0000E1P00 S IC RYZEN 100-000000295-40 3G BGA APU S	UC1  100-000000300-40 R7CZS_H_PC@ SA0000E1N00 S IC RYZEN 100-000000300-40 3.1G APU S	UC1  100-000000376-40 R3LCN_U_PC@ SA0000DWZ00 S IC RYZEN 100-000000376-40 2.6G APU S	UC1  100-000000375-40 R5LCN_U_PC@ SA0000DWY00 S IC RYZEN 100-000000375-40 2.1G BGA S	UC1  100-000000371-40 R7LCN_U_PC@ SA0000DWW00 S IC RYZEN 100-000000371-40 1.8G APU S

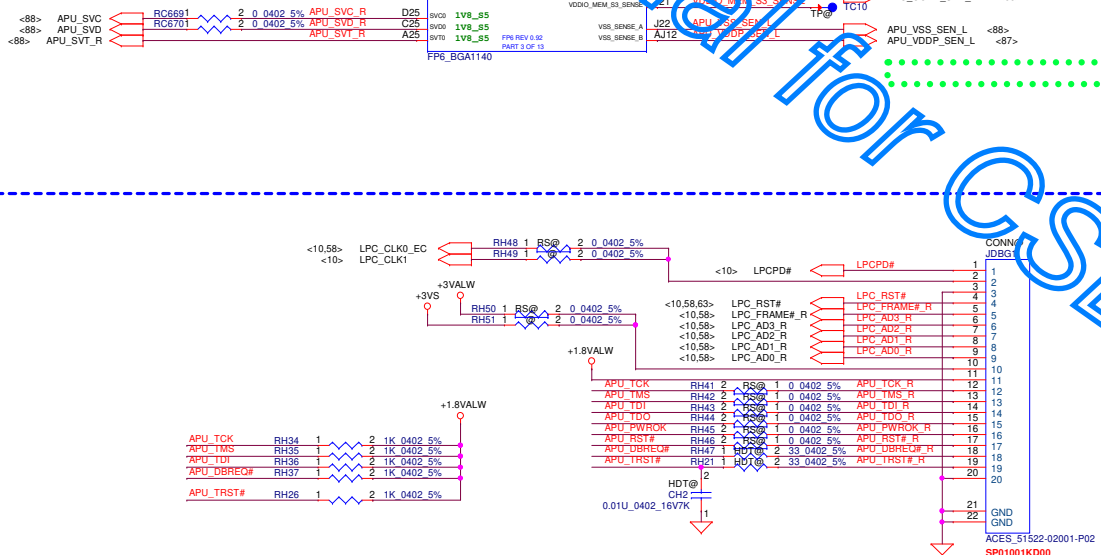
Main Func = CPU



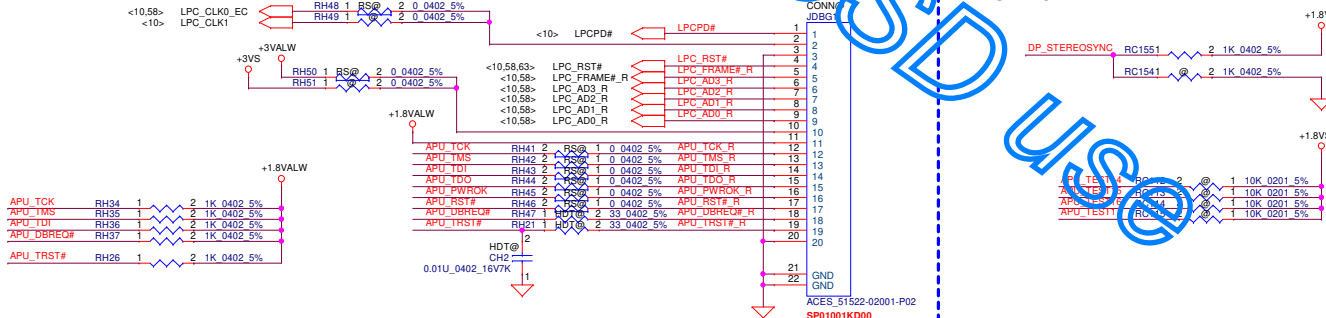
1



TESTPOINT

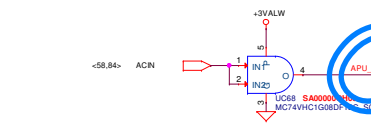


DP_STEREOSYNT

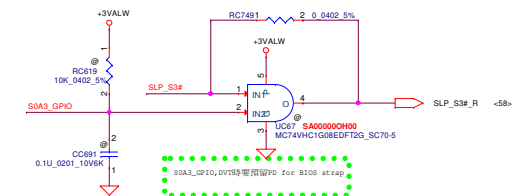
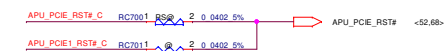
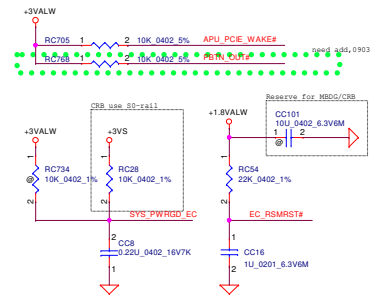


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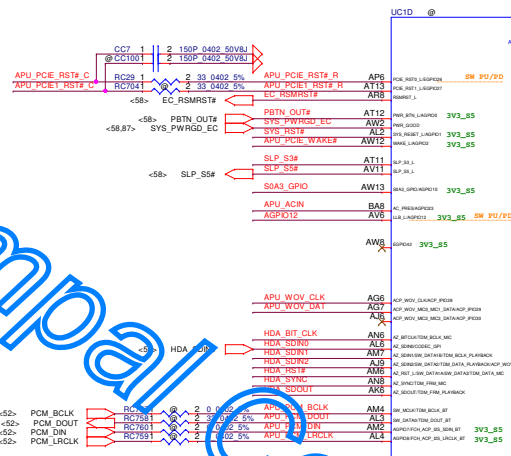
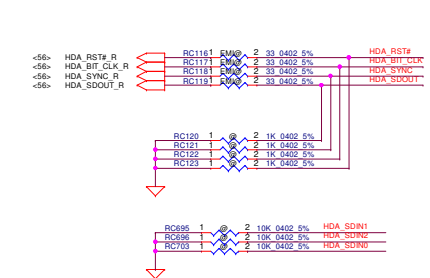
ACIN to APU



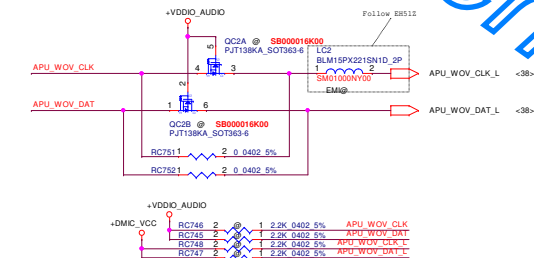
ACPI



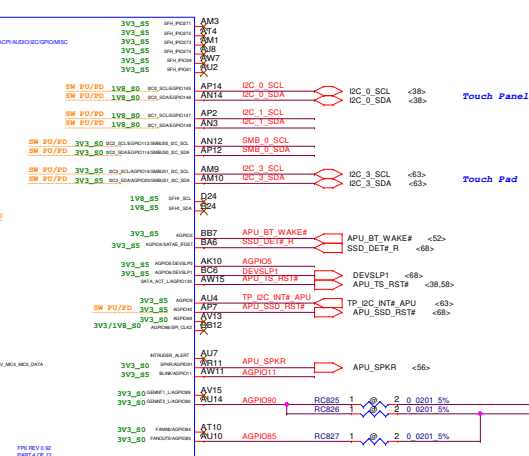
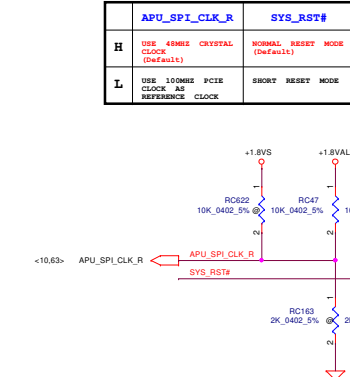
HDA



APU WOV

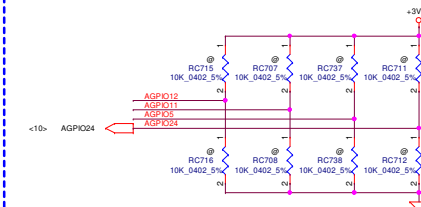


Strap Pin

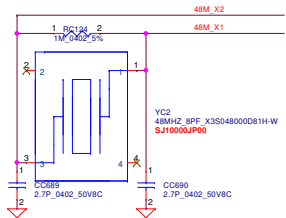


GPIO Table

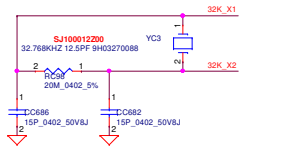
AGPIO[12:11:5:24]	LPDDR4X Vendor PN
0 0 0 0	MIC4_8GB
0 0 0 1	SAM4_8GB
0 0 1 0	HYN4_8GB
0 0 1 1	MIC4_16GB
0 1 0 0	SAM4_16GB
0 1 0 1	HYN4_16GB
0 1 1 0	
0 1 1 1	
1 0 0 0	
1 0 0 1	
1 0 1 0	
1 0 1 1	
1 1 0 0	
1 1 0 1	
1 1 1 0	
1 1 1 1	



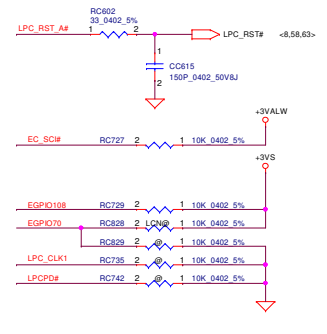
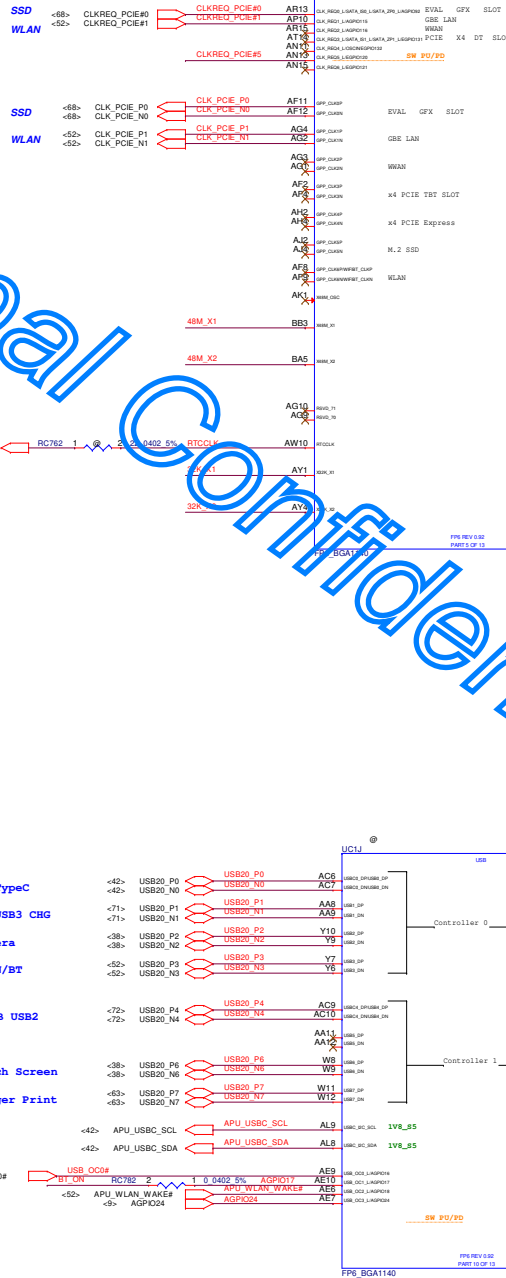
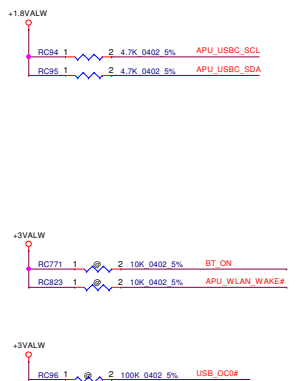
48MHz CRYSTAL



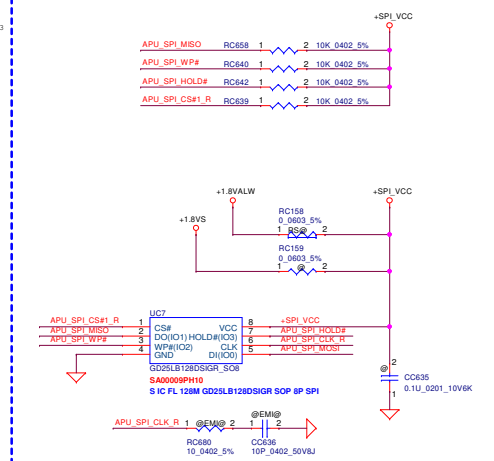
32.768KHz CRYSTAL



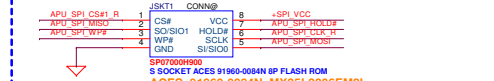
USB Function



16MB SPI ROM

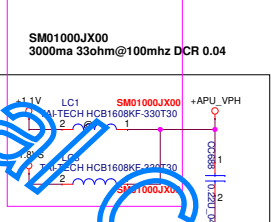
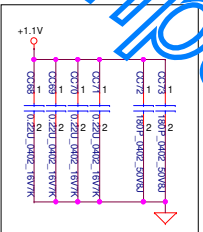
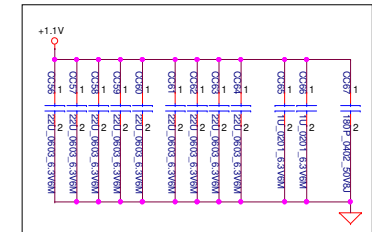
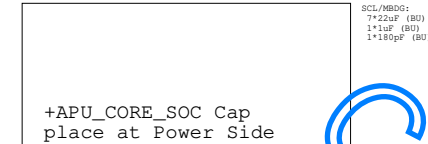


Co-Lay SPI Socket with UC7

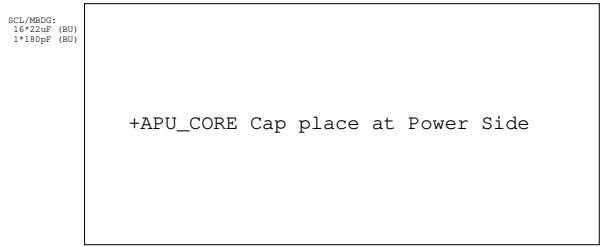
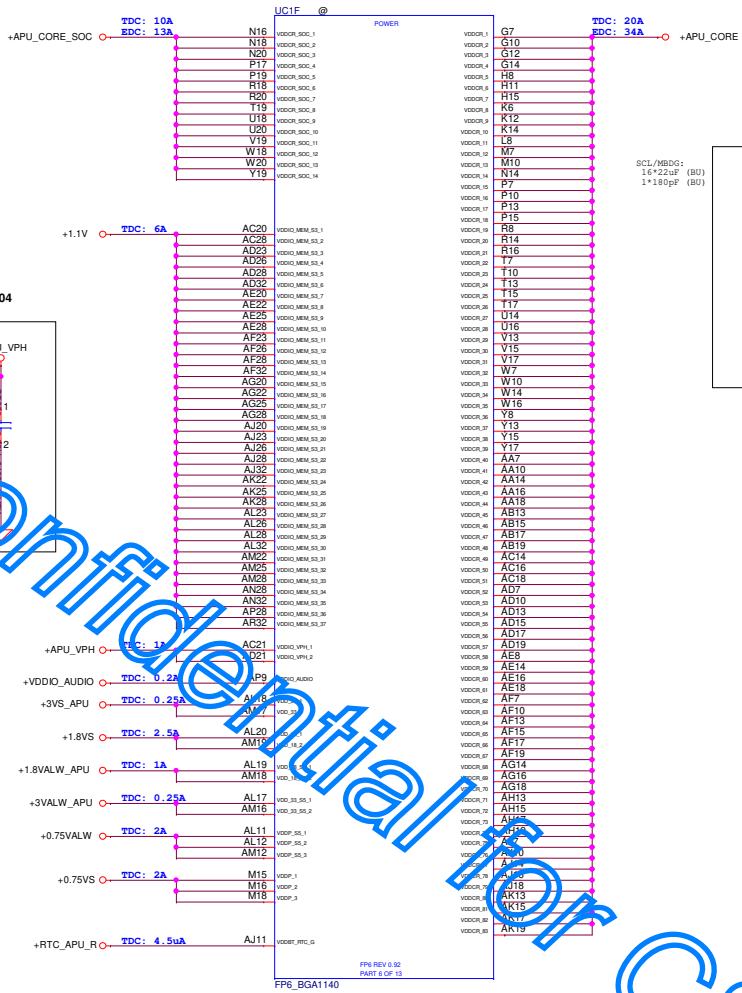
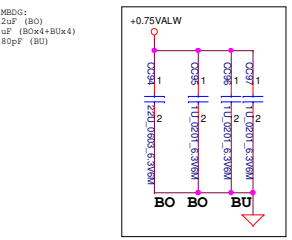
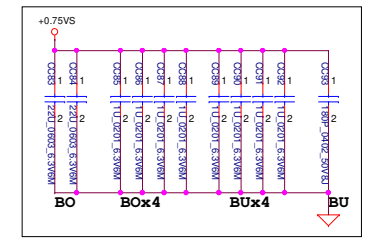
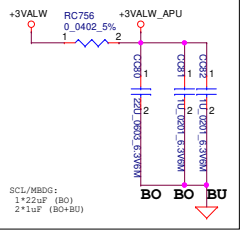
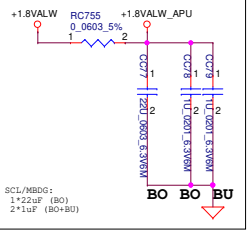
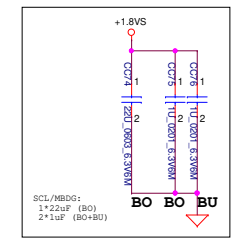
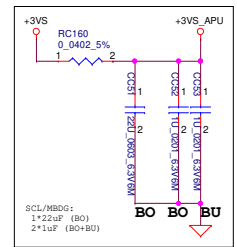
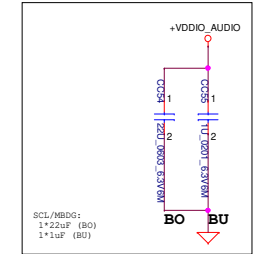


APU Strap Pin

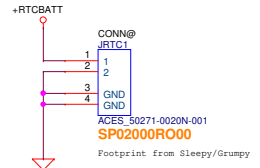
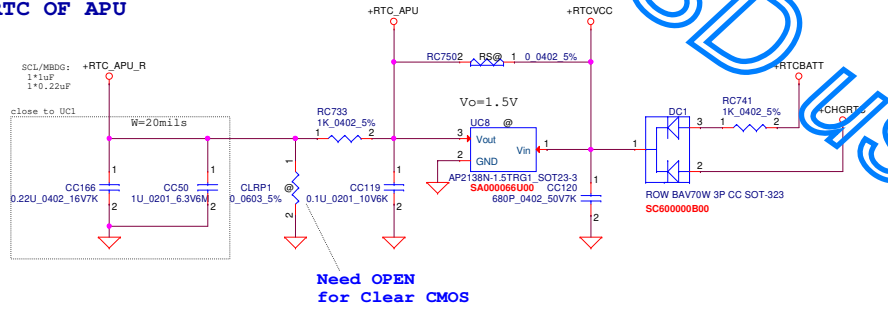
EGPIO120	EGPIO70	
L	L	Renoir-H
L	H	Lucienne
L	L	Cizanne-U
L	H	Cizanne-H



All BU(on bottom side under SOC) ACROSS VDDIO AND VSS SPLIT



RTC OF APU



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The diagram illustrates the pin connections for the BG1140 and FP6 modules. It is organized into three main sections: CAMERAS, UC1L @, and WIFI. Each section lists various pins and their corresponding functions, such as CAM_CLK, CAM_DC_SCL, CAM_DC_SDA, CAM_SHUTDOWN, and various data and control lines for the UC1L and WIFI modules. The diagram also includes a large diagonal watermark reading "Confidential for CSD Use".

CAMERAS

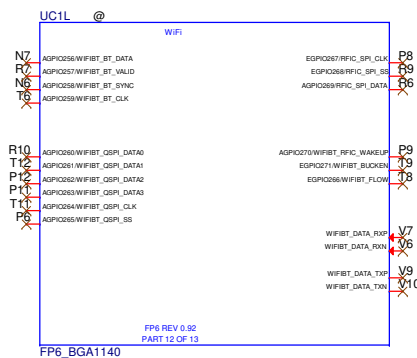
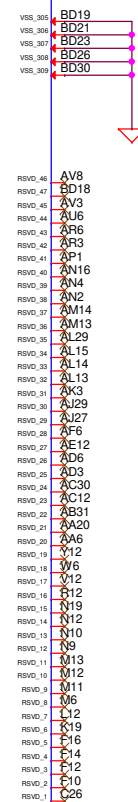
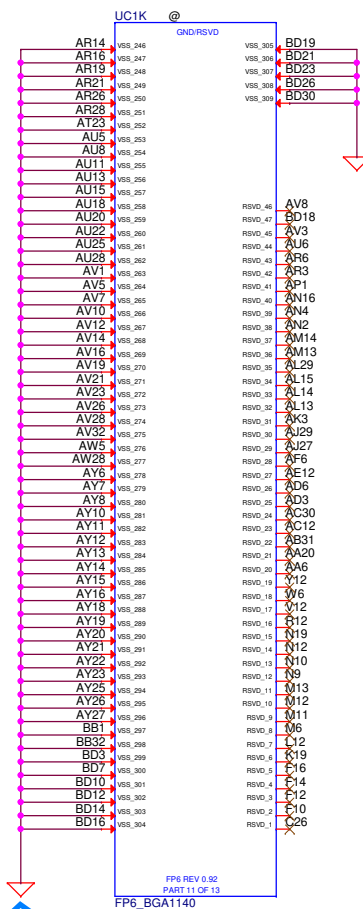
- CAM_CLK: A18
- CAM_DC_SCL: C18
- CAM_DC_SDA: B17
- CAM_SHUTDOWN: D17
- CAM_CLK: A13
- CAM_DC_SCL: B13
- CAM_DC_SDA: D13

UC1L @

- ASPC055/WFIBT_BT_DATA: N7
- ASPC057/WFIBT_BT_VALID: R7
- ASPC056/WFIBT_BT_SYNC: N6
- ASPC059/WFIBT_BT_CLK: T6
- ASPC060/WFIBT_GSPI_DATA0: R10
- ASPC061/WFIBT_GSPI_DATA1: T12
- ASPC062/WFIBT_GSPI_DATA2: P12
- ASPC063/WFIBT_GSPI_DATA3: P11
- ASPC064/WFIBT_GSPI_CLK: T11
- ASPC065/WFIBT_GSPI_SS: P6

WIFI

- EGPIC057/RFIC_SPL_CLK: P8
- EGPIC058/RFIC_SPL_SS: P9
- ASPC056/RFIC_SPL_DATA: P6
- ASPC070/WFIBT_RFIC_WAKEUP: P9
- EGPIC071/WFIBT_BLOCKEN: P9
- EGPIC066/WFIBT_FLDIO: P8
- WFIBT_DATA_RXN: V7
- WFIBT_DATA_RXN: V6
- WFIBT_DATA_TXN: V9
- WFIBT_DATA_TXN: V10



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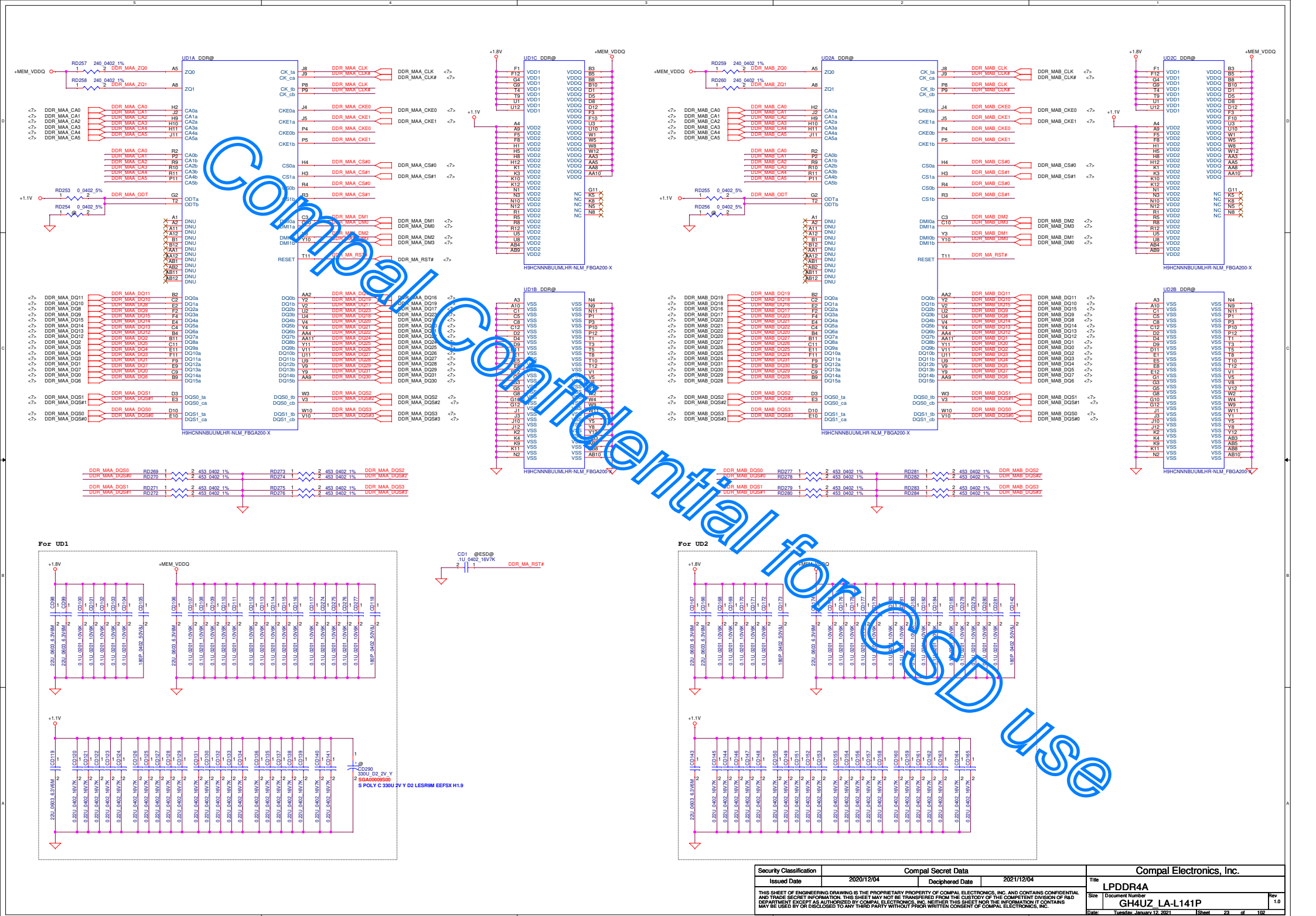
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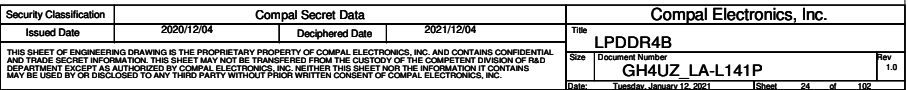
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Table 8. PC Default Unique Address I2C2 - Port 1

Default PC Unique Address	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0	1	1	1	1	0	0	0	RW

Note 1: Any bit is maskable for each port independently, providing firmware override of the PC address.

Table 9. PC Default Unique Address I2C2 - Port 2

Default PC Unique Address	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0	1	1	1	1	1	1	1	RW

Note 1: Any bit is maskable for each port independently, providing firmware override of the PC address.

Table 7. PC Default Unique Address I2C1 - Port 2

Default PC Unique Address	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0	1	1	1	1	1	1	1	RW

Note 1: Any bit is maskable for each port independently, providing firmware override of the PC address.

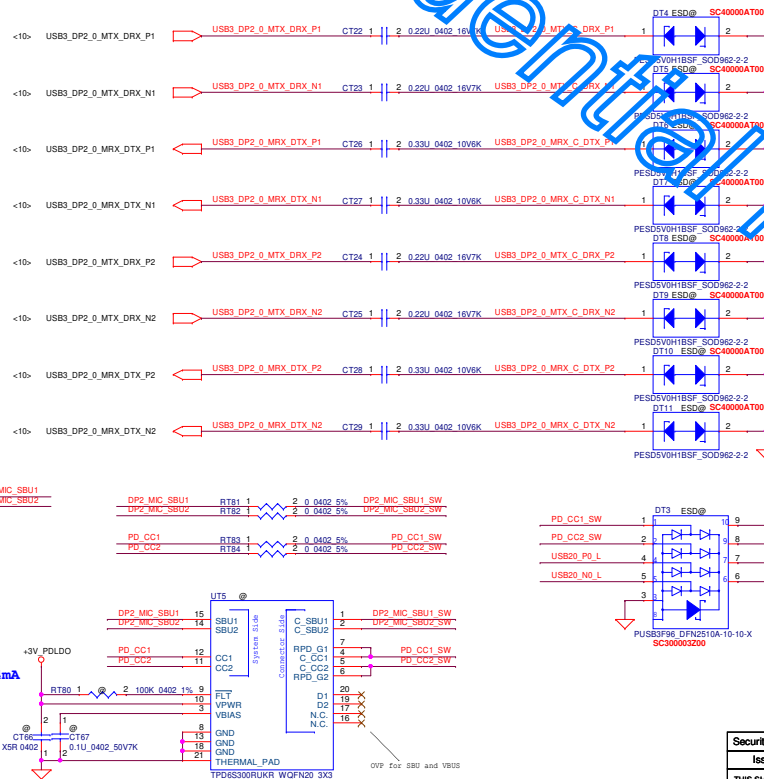
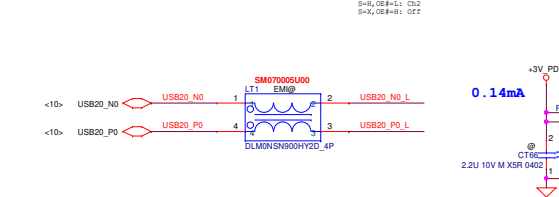
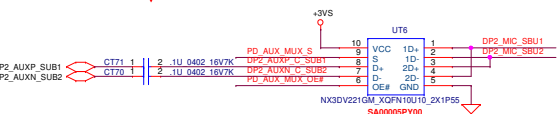
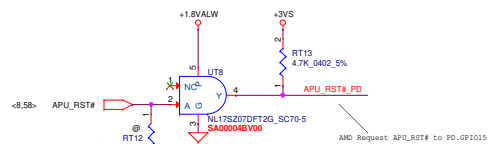
Figure 37. PC Address Divider

Table 10 lists the external divider needed to set bits 3-11 of the PC Unique Address.

Table 10. PC Address Selection

DIV_min	DIV_max	IC2C_ADDR_DECODE_C1	IC2C_ADDR_DECODE_C2	PC UNIQUE ADDRESS [3:11]
0.00	0.19	000	000	100
0.20	0.36	001	001	101
0.40	0.58	010	010	110
0.60	1.00	011	011	111

(1) External resistor tolerance of 1% is required. Resistor values should be chosen to yield a DIV value centered normally between listed MIN and MAX values.



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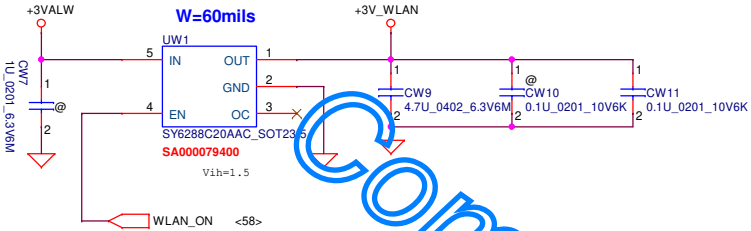
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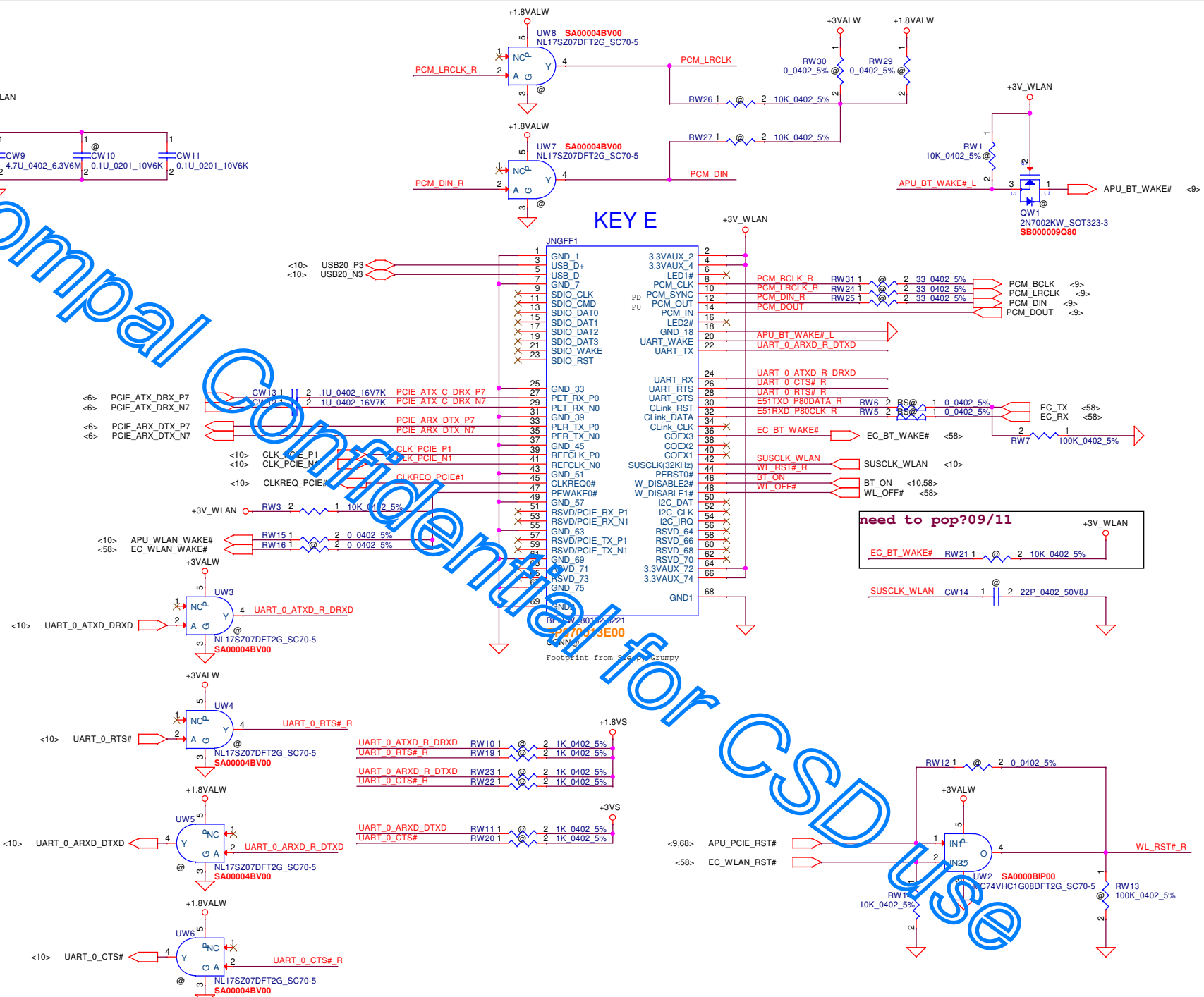
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Wireless LAN



NGFF WL+BT (KEY E)

74	1.3V	GND	75
72	1.3V	RESERVED/REFCLK1	73
70	UIM_Power_SRC/GPIO/PEWake#	RESERVED/REFCLK1	71
68	UIM_Power_SINK/CLKREQ#	GND	69
66	UIM_SWP/PERST#	Reserved/PERn1	67
64	RESERVED	Reserved/PERp1	65
62	ALERT# (I/O/3.3)	Reserved/PETn1	61
60	IDC_CLK (I/O/3.3)	Reserved/PETp1	59
58	IDC_DATA (I/O/3.3)	GND	57
56	W_DISABLE# (I/O/3.3V)	PEWake# (I/O/3.3V)	55
54	Reserved/W_DISABLE# (I/O/3.3V)	CLKREQ# (I/O/3.3V)	53
52	PERST# (I/O/3.3V)	CLKREQ# (I/O/3.3V)	51
50	SUSCLK(32KHz) (I/O/3.3V)	REFCLKNO	49
48	COEX1 (I/O/1.8V)	REFCLKP0	47
46	COEX2 (I/O/1.8V)	GND	45
44	COEX3 (I/O/1.8V)	PERn0	43
42	VENDOR DEFINED	PERp0	41
40	VENDOR DEFINED	GND	39
38	VENDOR DEFINED	PETn0	37
36	UART RTS (I/O/1.8V)	PETp0	35
34	UART CTS (I/O/1.8V)	GND	33
32	UART Tx (I/O/1.8V)	GND	31
30	UART Rx (I/O/1.8V)	SDIO Reserve (I/O/1.8V)	29
28	UART Wake# (I/O/3.3V)	SDIO DAT2 (I/O/1.8V)	27
26	GND	SDIO DAT1 (I/O/1.8V)	25
24	LED#2 (I/O/1.8V)	SDIO CMD (I/O/1.8V)	23
22	PCM_IN/IS_SD_OUT (I/O/1.8V)	SDIO CLK (I/O/1.8V)	21
20	PCM_IN/IS_SD_IN (I/O/1.8V)	GND	19
18	PCM_SYNC/IS_VS (I/O/1.8V)	GND	17
16	PCM_CLK/IS_SCK (I/O/1.8V)	GND	15
14	LED#1 (I/O/1.8V)	GND	13
12	LED#1 (I/O/1.8V)	GND	11
10	LED#1 (I/O/1.8V)	GND	9
8	LED#1 (I/O/1.8V)	GND	7
6	LED#1 (I/O/1.8V)	GND	5
4	LED#1 (I/O/1.8V)	GND	3
2	LED#1 (I/O/1.8V)	GND	1



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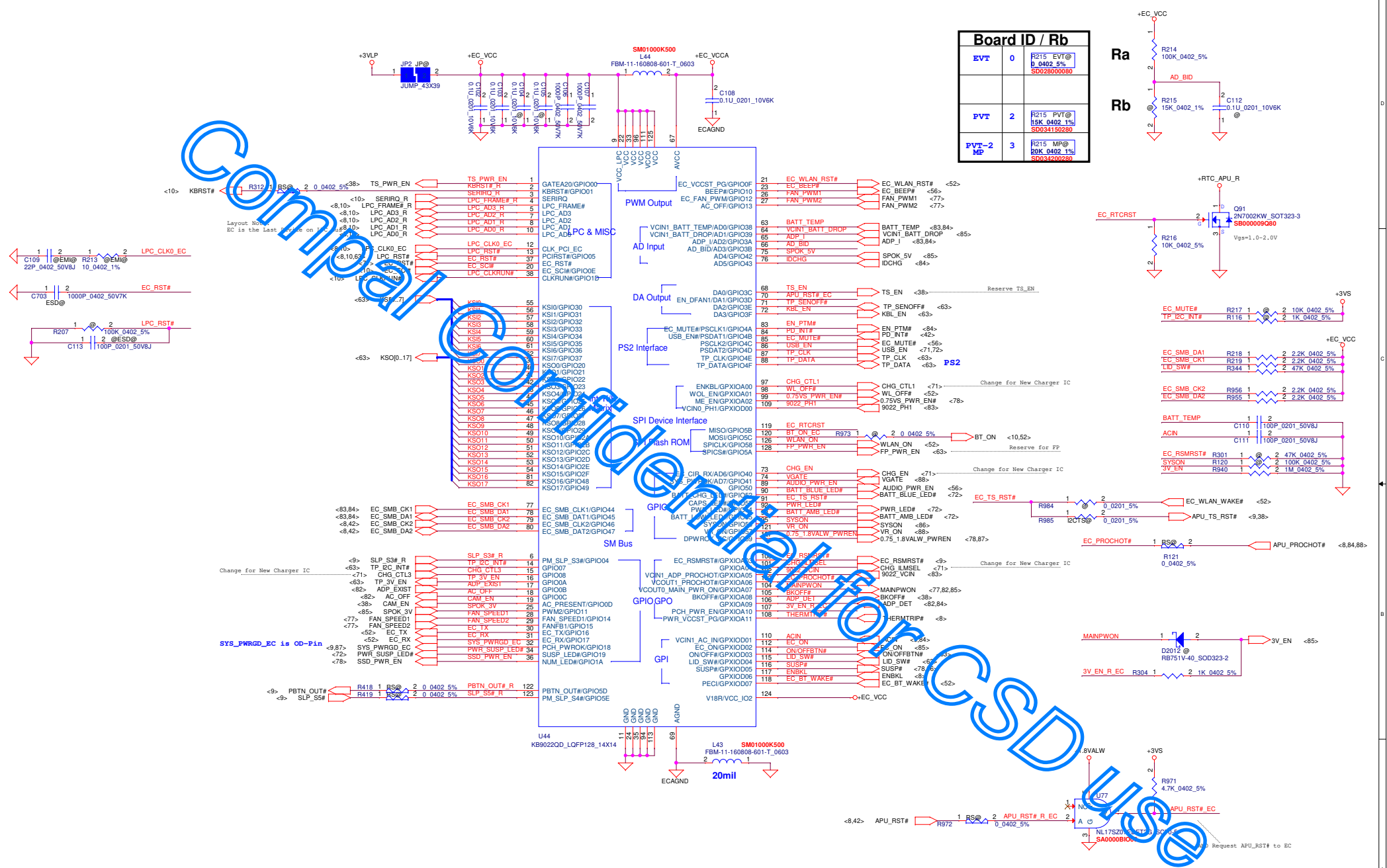
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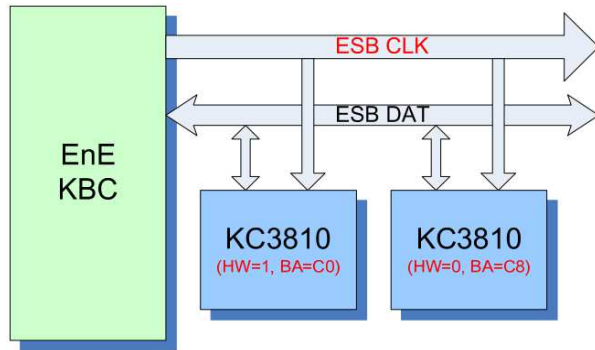
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Some EC GPIO maybe change to extend IO control:TBD

Some EC GPIO maybe change to extend IO control:TBD



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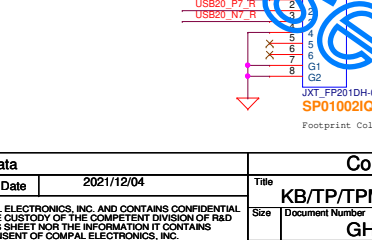
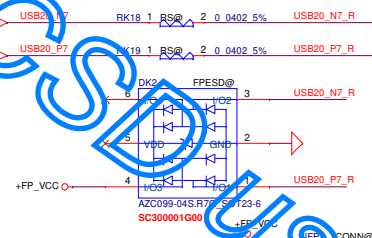
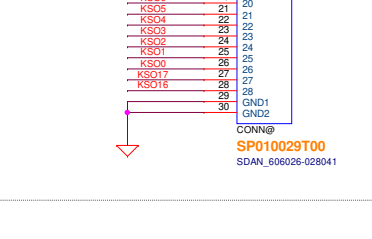
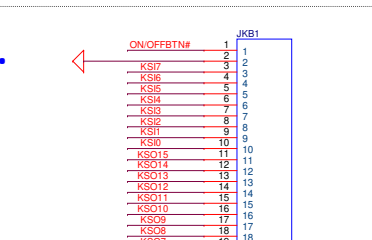
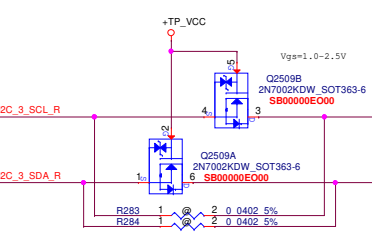
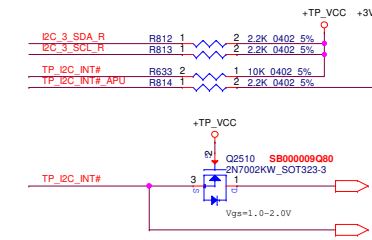
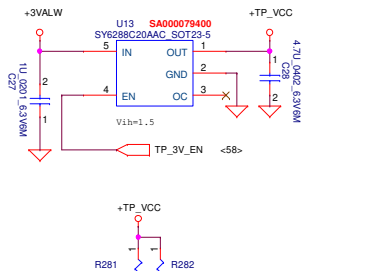
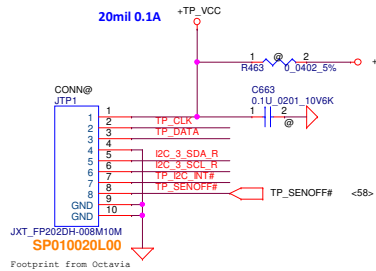
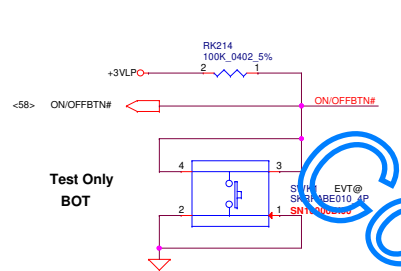
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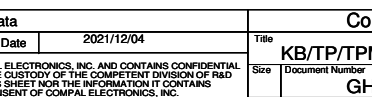
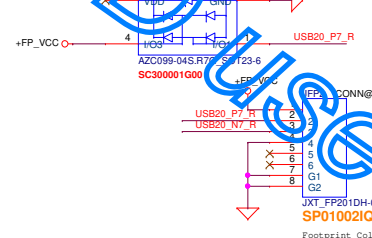
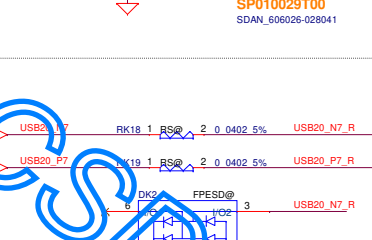
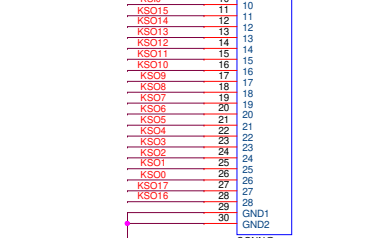
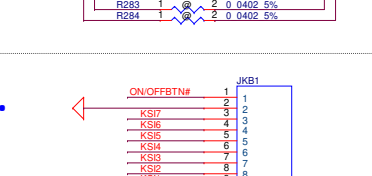
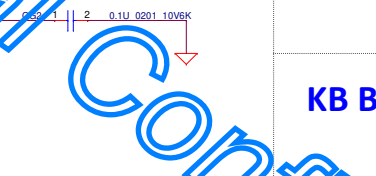
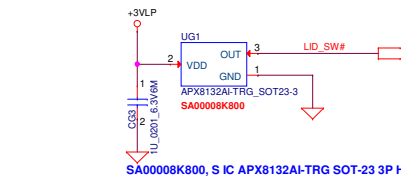
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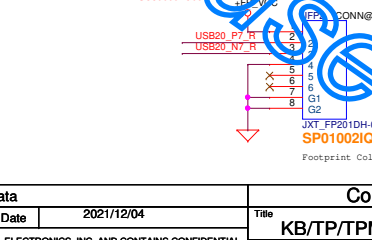
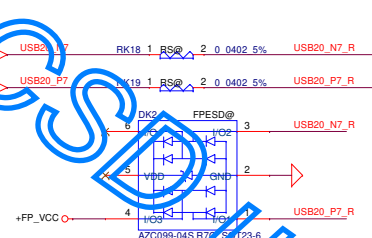
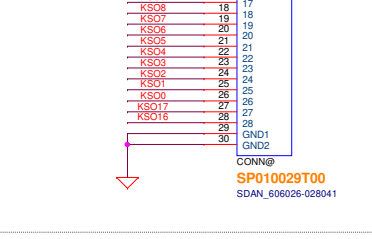
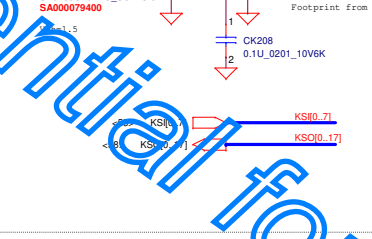
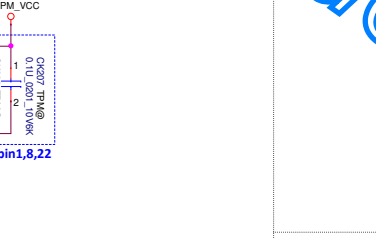
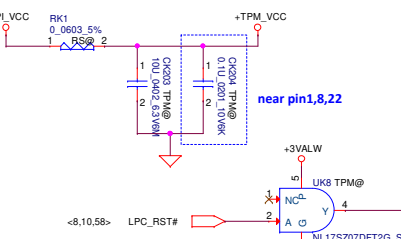
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Lid Switch (Hall Effect Switch)

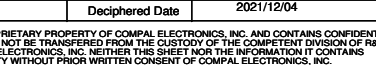
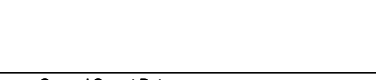
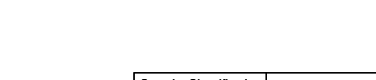
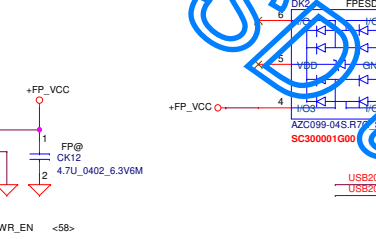
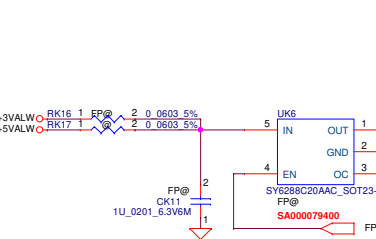
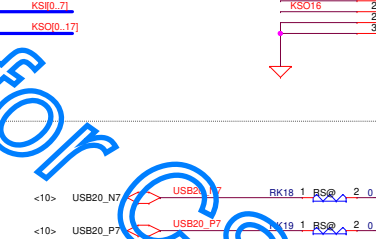
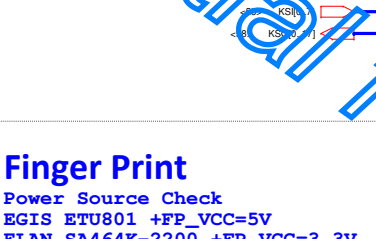
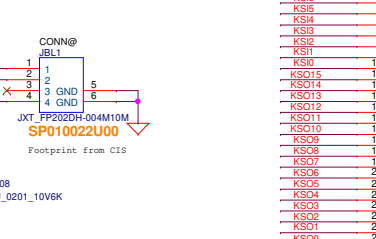
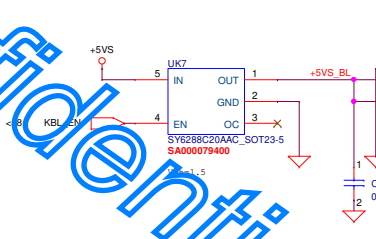


TPM



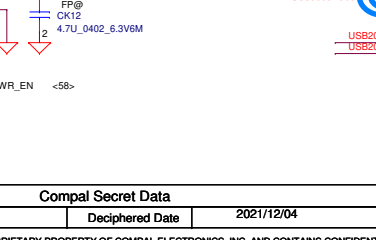
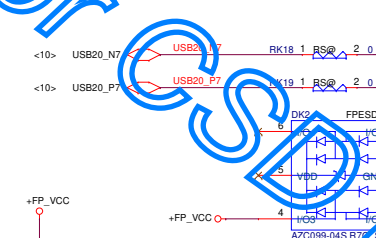
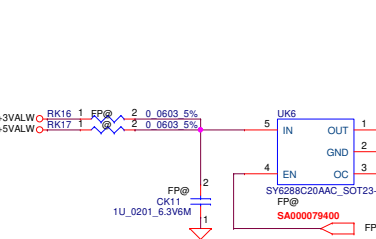
KB BackLight

KB Conn.



Finger Print

Power Source Check
EGIS ETU801 +FP_VCC=5V
ELAN SA464K-2200 +FP_VCC=3.3V



JFP1	
PIN	SA464K-2200
1	+FP_VCC (3V)
2	D+
3	D-
4	GND
5	NC
6	NC
7	NC
8	NC

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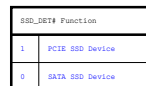
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PCIE/SATA NGFF SSD Conn.



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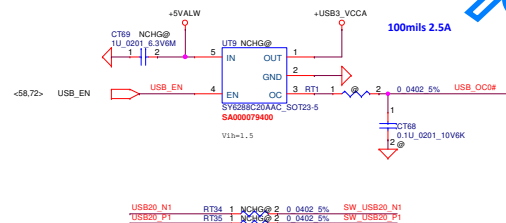
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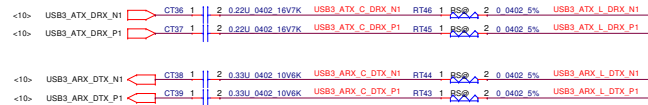
Non-Charger Co-lay



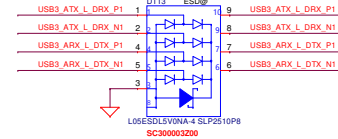
USB Host Charger Truth Table

CHG_EN	CTL1	CTL2	CTL3	ILIM_SEL	MODE	Current Limit Setting	Note
0	0	1	0	1	SDP1-OFF	ILIM_H	Port power off
1	0	1	0	1	SDP1	ILIM_H	Data Lines Connected
1	0	1	1	1	DCP Auto	ILIM_H	Data Lines Disconnected
1	1	1	1	1	CDP	ILIM_H	Data Lines Connected

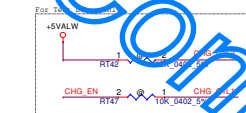
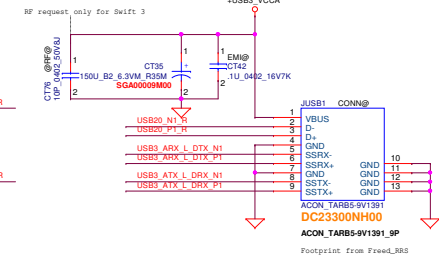
USB3.0 (Port 1)



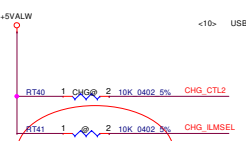
For ESD request



USB3.0 Conn.



0904 vendor recommend
0904 reserve VIN & VOUT 1206 for QFN current measure

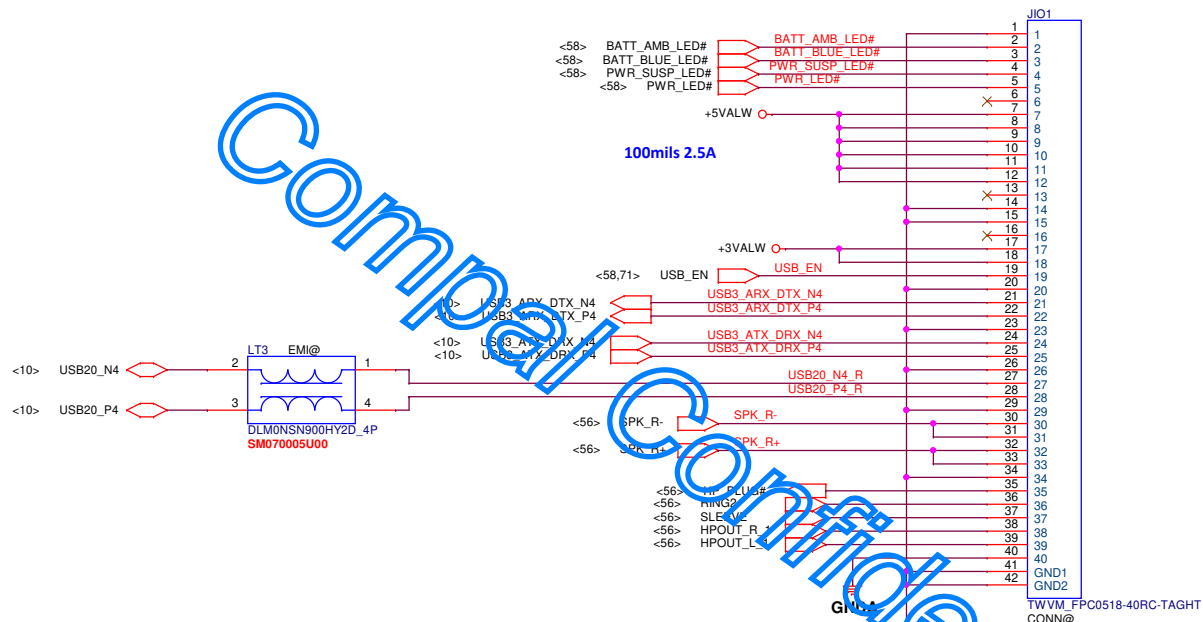


0911 Rerserve PU, vendor suggest to EC support if future need support SDP2

ILM R vaule
Ios (mA) =50250/R(Kohm)
ILIM_Hi=2273mA
ILIM_L=1288mA

0831 Reserve ILIM_L R as vendor recommend

IO/B



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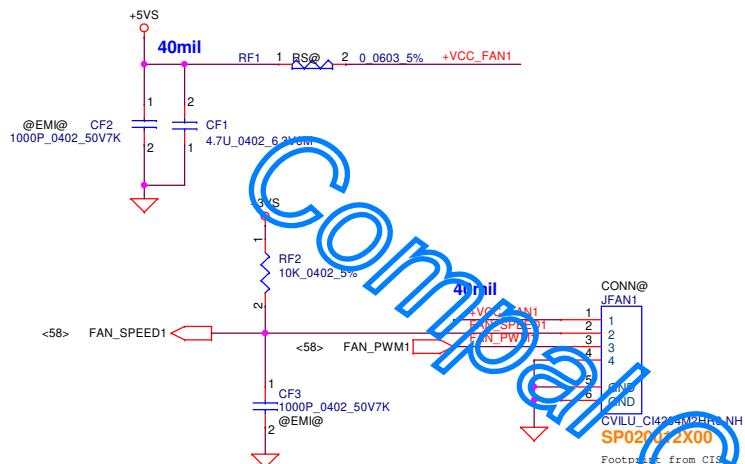
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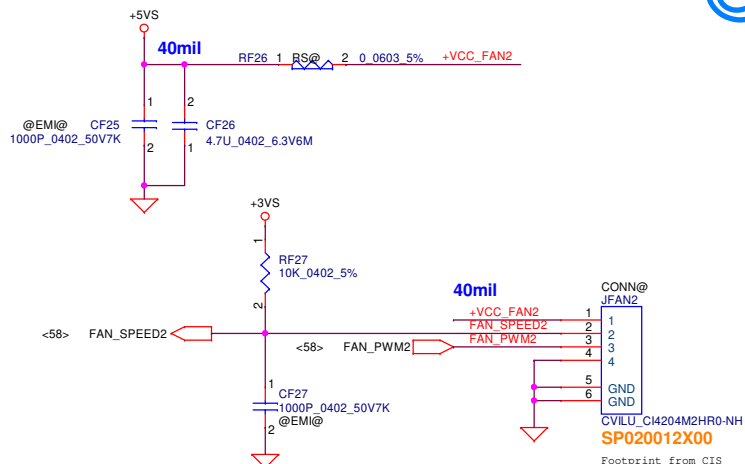
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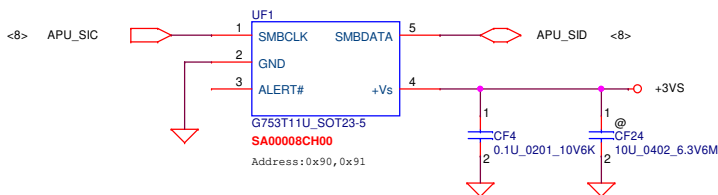
FAN1 Conn



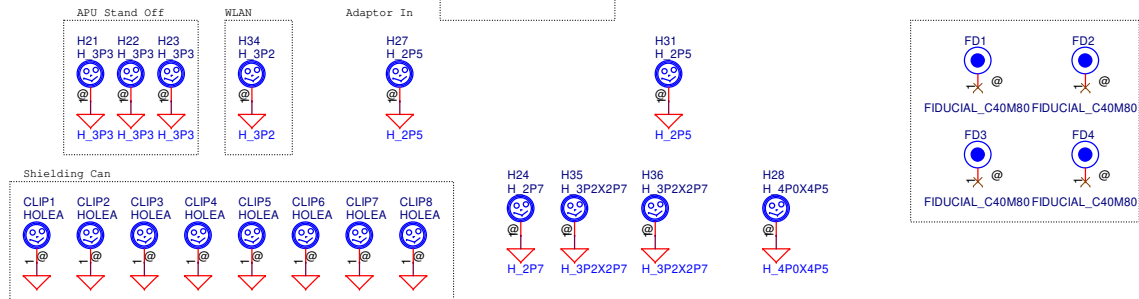
FAN2 Conn Reserve



Thermal Sensor IEC



Screw Hole



Reset Circuit

BI_GATE PH to +RTCVCC at PWR side

<83> BI_GATE

BI_GATE

BI_GATE

BI_GATE

BI_GATE

BI_GATE

BI_GATE

BI_GATE

BI_GATE

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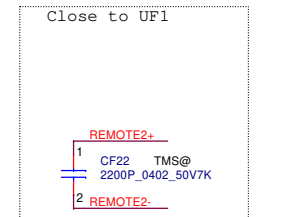
BI_GATE

BI_GATE

BI_GATE

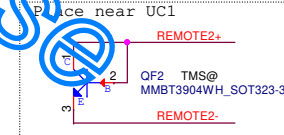
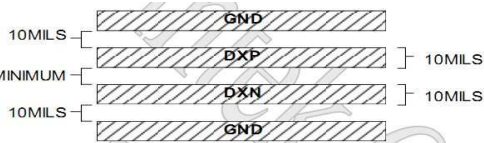
BI_GATE

THERMAL SENSOR

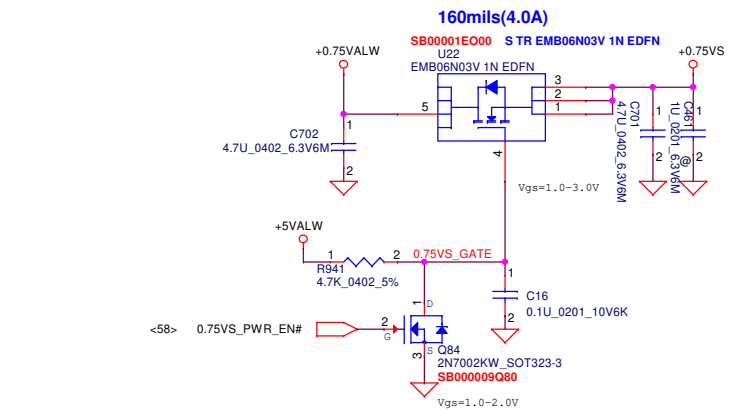
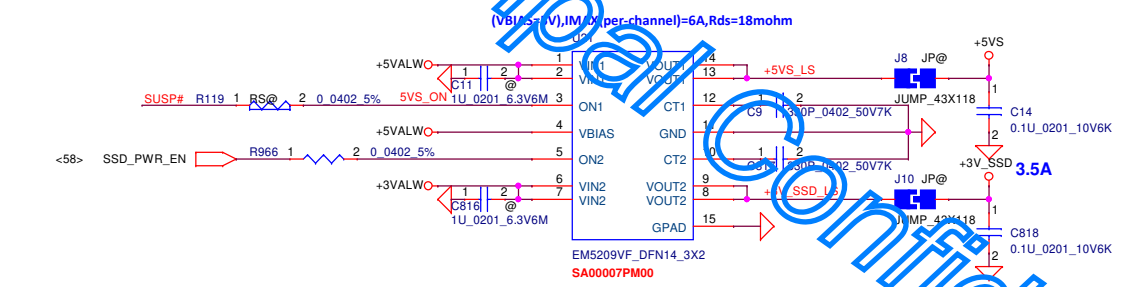
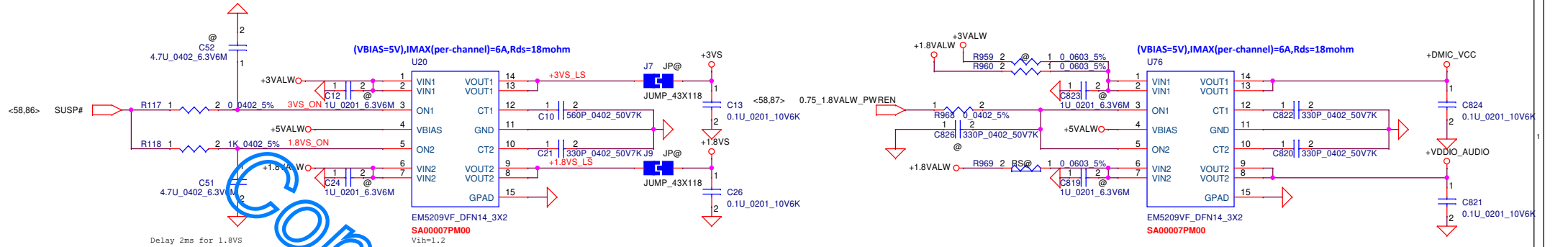


Part Number	SMBus Address
F75303M	1001_101xb

REMOTE1,2 (+/-):
Trace length: <8"



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